**ENGI - 5331 - Digital ASIC Design**

Course Project – Full Adder

Instructor : Dr. Yushi Zhou

By:

Arun Jebasingh Joseph Gnanaraj Asir (0899979)

Ajesh Wilfred Prakash (0883163)



**TEAM REPORT: EDITORIAL SIGN OFF**

|  |  |
| --- | --- |
| **Group Member**  *(please type or print name clearly)* | **Percentage of Contribution**  *(equal percentages indicate equal contribution)* |
| Arun Jebasingh Joseph Gnanaraj Asir | 50% |
| Ajesh Wilfred Prakash | 50% |

By signing below, We acknowledge that our group has agreed with the above assessment of group member contribution.

By signing below, We acknowledge that We have read the final submission and that We have offered comments and corrections to the same. To the best of our knowledge, everything in this submission represents original work. Any ideas or concepts that are not original have been referenced.

|  |  |  |
| --- | --- | --- |
| **Team Member Names**  *(please print or type clearly)* | **Team Member Signatures:** | **Date:** |
| Arun Jebasingh Joseph Gnanaraj Asir |  | Dec 04, 2018 |
| Ajesh Wilfred Prakash |  | Dec 04, 2018 |

Abstract:

Every microcontroller and microprocessor plays a vital role in any digital circuit consists of an Arithmetic and Logic unit which basically holds a adder logic circuit. There are different variety of adders